Course Outcome: Digital System Design Lab - [ EC307PC ]

CO1: Ability to realize the boolean expressions using gates

CO2: Ability to design logic gates using universal gates, full adder and subtractor.

CO3: An ability to design and realization of a 4-bit gray to binary and binary to gray converter, 8-bit parallel load and serial out shift register using flip-flops.

CO4: Design and realization of asynchronous counters using flip-flops, 8x1 mux using 2x1 mux 10, 4 bit comparator