

## BIO SKETCH OF VEERASWAMY D



Name :VEERASWAMY DHARAVATH  
 Designation : Assistant Professor  
 Total Experience :6.6 Yrs.  
 Email:veeraswamyd.ece@hitam.org  
 Mobile: +91-9908763518

### Specialization

Electronics and Communication Engineering

### Academic Qualification

B.Tech (ECE)  
 M.Tech (ECE)

### Total Teaching Experience

6.6 Yrs.

### Publication Details

#### International Journals:

IEEE Explore "Low Temperature Process Grown Nanocluster Carbon Films in Applied Electronics," 2020 International Conference on Smart Electronics and Communication (ICOSEC), Trichy, India, 2020, pp.  
 Mukts Shabd Journal:Implementation of Real Contact Areas into Sheet metal Forming Simulations using Digital Spotting Images,Volume X, Issue VI June 2021 Page No :579-589  
 Mukts Shabd Journal :A High Speed VLSI Architecture of square root brent kung carry select adder Volume IX, Issue XI November 2020 Page No :70-72  
 (IJSETR) Efficient Method of Power Saving Topologically-Compressed, with 21-Transistors Flip-Flop using Multi Mode Switches, ISSN 2319-8885 Vol.06, Issue.02, January-2017, Pages:0216-0222  
 (IJATIR) Reducing the Leakage Power in CMOS Circuit by using Biasing and Body Biasing Modes, ISSN 2348-2370 Vol.09, Issue.01, January-2017, Pages:0100-0106.  
 (IJTR) Handheld Money Deposit And Withdrawal Cell Banking Gadget, Volume No.4, Issue No.5, August – September 2016, 4190-4192.  
 Intend Of Elegant Anti Stealing System For Vehicle Refuge With Iot Technology, Volume 7, Issue 5, 2018 May 2018, ISSN NO (Online) : 2319-7064  
 (IJSRR) Intend Of Elegant Anti Stealing System For Vehicle Refuge With Iot Technology, Volume 7, Issue 5, 2018 May 2018, ISSN NO: 2279-543X

#### International Conferences:

IEEE Explore "Low Temperature Process Grown Nanocluster Carbon Films in Applied Electronics," 2020 International Conference on Smart Electronics and Communication (ICOSEC), Trichy, India, 2020, pp.

### Workshops / Seminars / Conferences / Training Programs Attended

Conducted webinar on Insight into Nano Technology  
 Conducted 5 Days FDP on APPLIED IOT WITH NODE  
 Attended 3 days FDP on VLSI IC Design Flow conducted by Anurag University  
 Participated in AICTE Sponsored One Week Online Short Term Training Program (STTP) PHASE-III on " Medical Image Processing & It's Applications in Automated Disease Detection "  
 Attended one week AICTE-ISTE STTP on IoT with FPGA and Silicon on Chip.  
 Attended AICTE-ISTE approved Orientation/Refresher Programme on "Machine Learning for All  
 Natural language processing  
 ISTE STTP on "CMOS,Mixed signal and Radio frequency VLSI Design", IIT Kharagpur from 30 jan.-4 Feb 2017.  
 Attended a webinar on Solar Power Inverters  
 Attended a webinar on AI & Robotics  
 Participated 3 days work shop on NAAC Assesment & Accreditation : A Step by Step Process  
 Attended a webinar on "Art of Research Paper Writing"  
 Webinar on "role of telemetry technology for emerging applications in aircraft research & development"  
 Attended a webinar on Friends and Challenges in the progress of E-mobility in India.  
 ISTE STTP on "CMOS,Mixed signal and Radio frequency VLSI Design", IIT Kharagpur.  
 Workshop on "Digital design through Arduino".  
 FDP on "Hands-on Intensive MATLAB Workshop on Digital Image Analysis for Pattern Recognition & Classification Techniques"  
 Workshop on "Real time applications on Advanced Embedded &VLSI systems".